

FIG.1 PRIOR ART

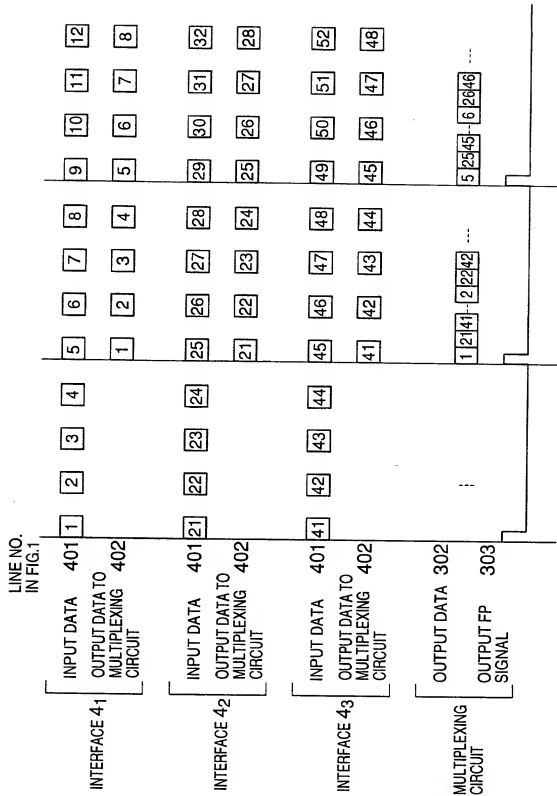


FIG.2 PRIOR ART

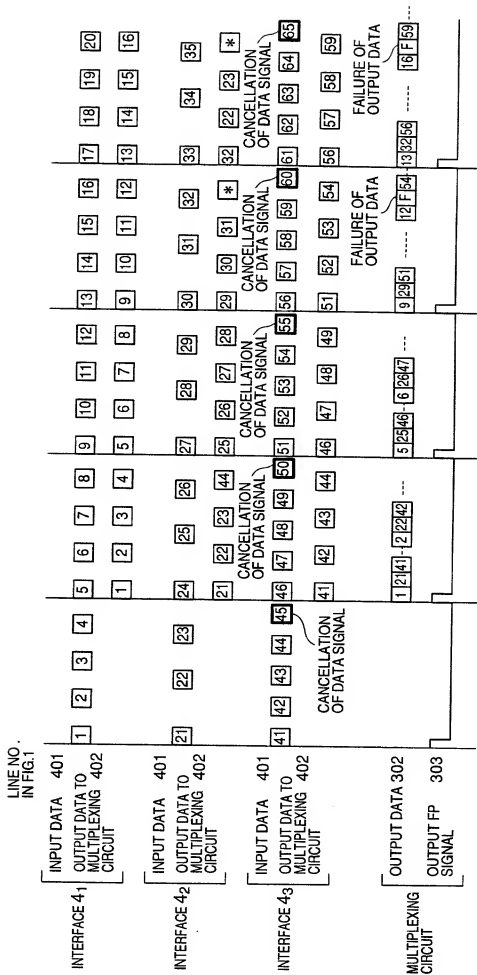


FIG.3 PRIOR ART

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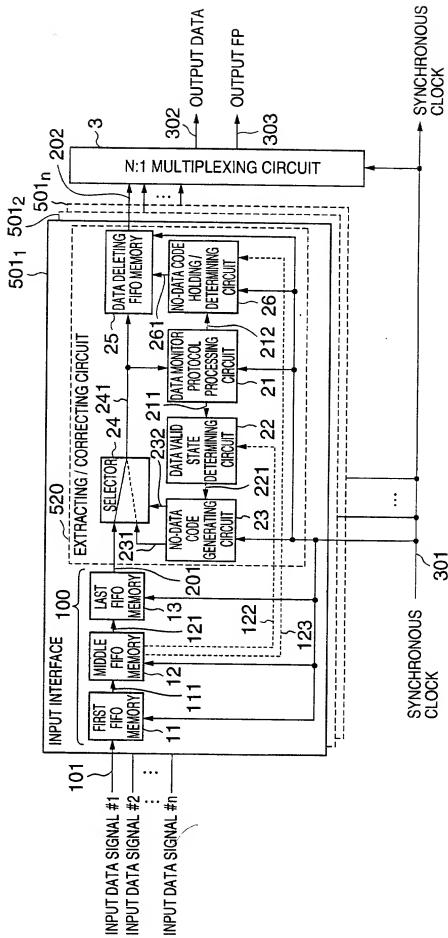


FIG.4

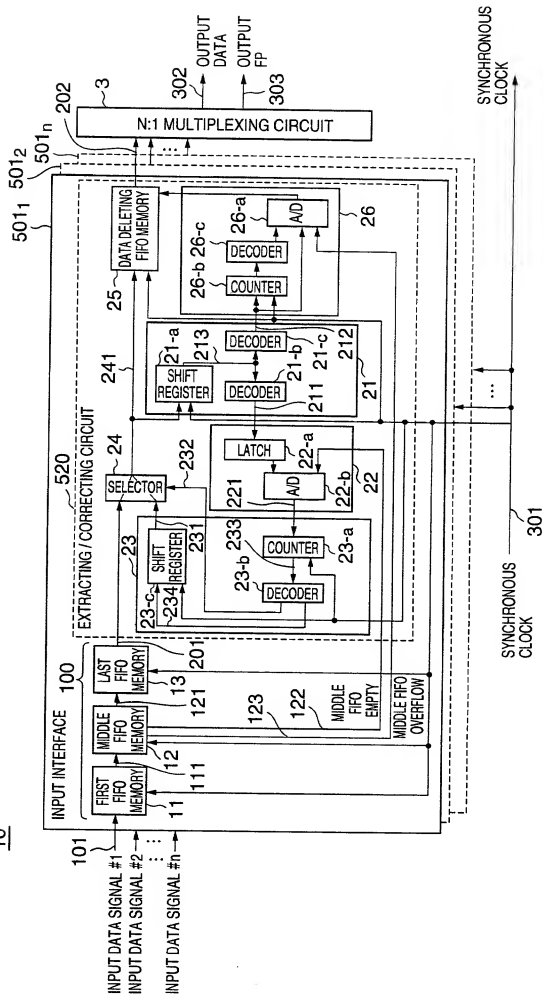


FIG. 5

2012030522001

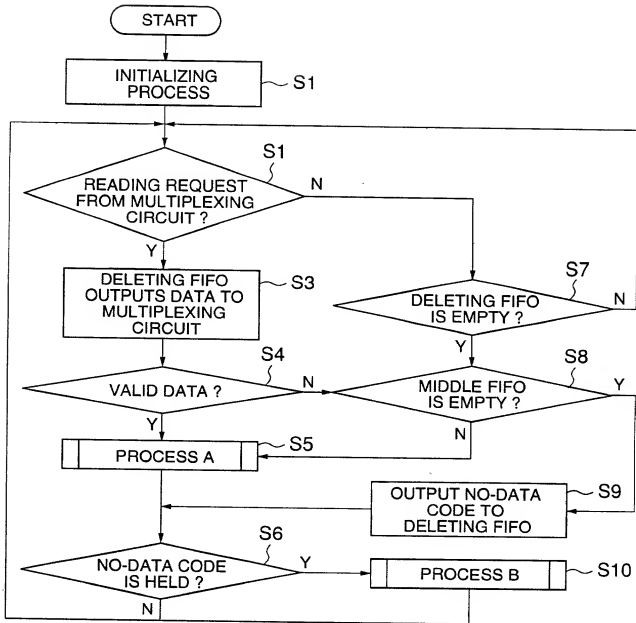


FIG.6

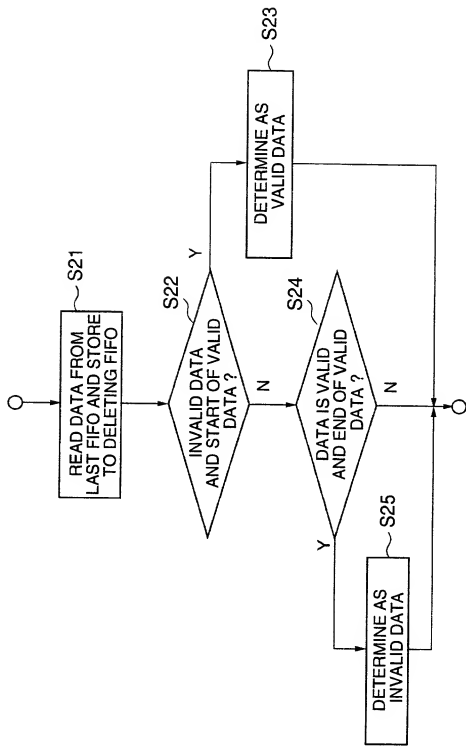


FIG.7

FIG.8

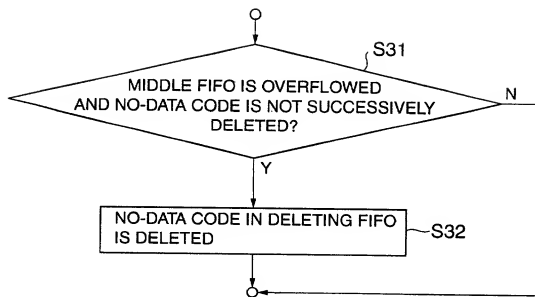




FIG.9

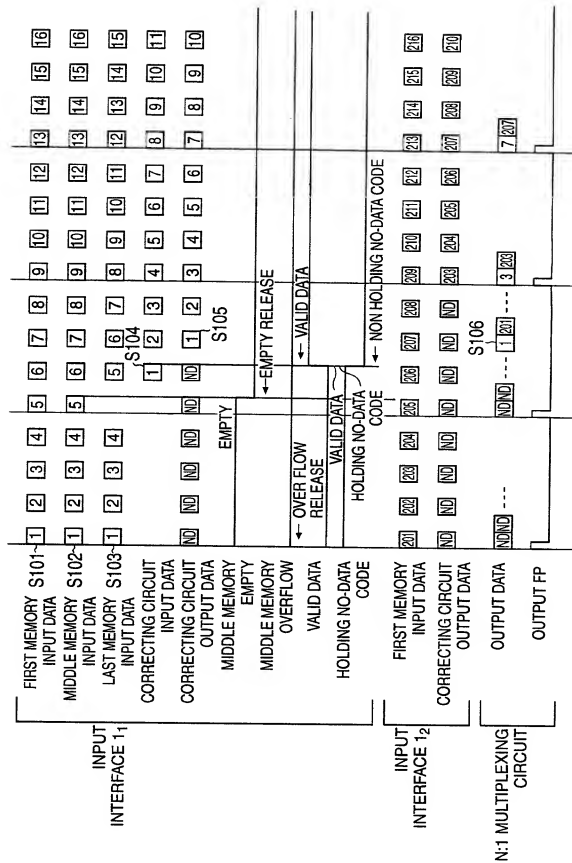






FIG.12

INPUT DATA	S201 S203												
	1	2	3	4	5	6	7	8	9	10	11	12	13
MIDDLE FIFO MEMORY													
LAST FIFO MEMORY					5								
				4	4								
				3	3								
				2	2								
CIRCUIT INPUT	1	1	1	1	1	2	2	3	4	5	6	7	8
	0	0	0	0	0	1	2	3	4	5	6	7	8
CIRCUIT OUTPUT	0	0	0	0	0	0	1	2	3	4	5	6	7
	0	0	0	0	0	0	1	2	3	4	5	6	7
S202 S204 S205 S206 S207													
EMPTY	1	1	1	1	1	0	1	1	1	1	1	1	1
VALID DATA	0	0	0	0	0	0	1	1	1	1	1	1	1
	0	0	0	0	0	0	1	1	1	1	1	1	1

**FIG. 13**

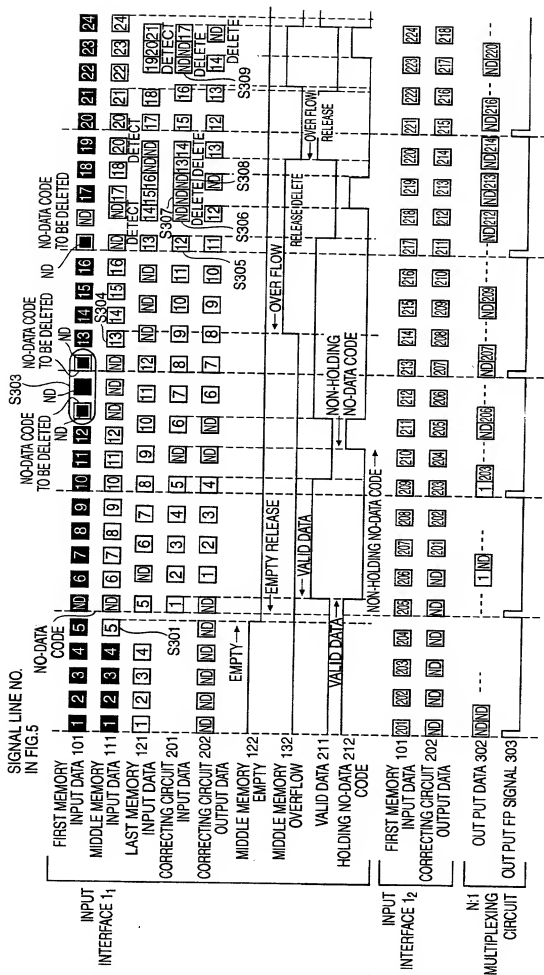


FIG.14

INPUT DATA	1	2	3	4	5	IC	6	7	8	9	10	11	12	(IC)	IC	(IC)	13	14	15	16
FIRST FIFO MEMORY																				
MIDDLE FIFO MEMORY																				
LAST FIFO MEMORY																				
CIRCUIT INPUT	0	0	0	0	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CIRCUIT BUFFER	0	0	0	0	0	0	1	1	2	3	4	5	6	7	8	9	10	11	12	13
CIRCUIT OUTPUT	0	0	0	0	0	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14
EMPTY	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
OVERFLOW	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
VALID DATA	0	0	0	0	0	0	1	1	1	1	1	0	0	0	1	1	1	1	1	1
HOLDING NO.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
DATA CODE	1	1	1	1	1	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0

INPUT DATA	(IC)	IC				17	18			19	20	21		22	23					24
FIRST FIFO MEMORY																				
MIDDLE FIFO MEMORY	(IC)	(IC)	IC	IC	IC	17	17	17	18	19	20	21	22	22	23	23	23	23	23	24
LAST FIFO MEMORY	(IC)	(IC)	IC	IC	IC	13	13	13	14	14	15	16	16	16	17	17	17	17	17	18
CIRCUIT INPUT	12	(IC)	(IC)	(IC)	(IC)	13	14	15	16	16	16	16	16	16	16	16	16	16	16	17
CIRCUIT BUFFER	11	(IC)	(IC)	(IC)	(IC)	13	14	15	16	16	16	16	16	16	16	16	16	16	16	17
CIRCUIT OUTPUT	11	(IC)	(IC)	(IC)	(IC)	13	14	15	16	16	16	16	16	16	16	16	16	16	16	17
EMPTY	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
OVERFLOW	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
VALID DATA	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
HOLDING NO.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
DATA CODE	0	0	0	(1)	1	1	(1)	(1)	0	0	0	0	0	0	0	(1)	(1)	1	0	0

S305 S306 S307 S308

S309